

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	38	Lim ADJ Chee ADJ How.in.	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:03
S2	1	((NEAL) near2 (WOLFF)).INV.	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:03
S4	1	WOLFF ADJ1 NEAL.in.	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:05
S5	5	(reference AND bypass AND feedback) adj clock	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:06
S6	1	(reference AND bypass AND feedback) adj clock.clm.	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:08
S11	74	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:13
S10	37	("714"/\$ "324"/\$).ccls. and clock adj generat\$3 and reference adj clock near3 PLL	US-PGPUB; USPAT	OR	OFF	2006/12/07 12:13
S12	30	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL	US-PGPUB	OR	OFF	2006/12/07 12:14
S13	44	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL	USPAT	OR	OFF	2006/12/07 12:20
S14	38	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL and (align\$5 synchron\$6)	USPAT	OR	OFF	2006/12/07 12:21
S15	39	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL and (align\$5 synchron\$6)	USPAT	OR	ON	2006/12/07 12:34
S17	13	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL with (align\$5 synchron\$6)	US-PGPUB; USPAT	OR	ON	2006/12/07 12:35
S16	64	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL and (align\$5 synchron\$6)	US-PGPUB; USPAT	OR	ON	2006/12/07 12:35
S18	13	("714"/\$ "324"/\$).ccls. and reference adj clock near3 PLL with (align\$5 synchron\$6 adjust\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 12:38
S22	1	"20050012524".PN.	US-PGPUB	OR	OFF	2006/12/07 12:46
S23	1	"20050063502".PN.	US-PGPUB	OR	OFF	2006/12/07 12:48
S21	5	("714"/\$ "324"/\$).ccls. and reference adj clock and PLL with edge with compar\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 12:54
S24	12	("714"/\$ "324"/\$).ccls. and PLL with edge with compar\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 12:56
S25	58	714/700.ccls. and PLL	US-PGPUB; USPAT	OR	ON	2006/12/07 12:57

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S26	28	714/700.ccls. and PLL and jitter\$3	US-PGPUB; USPAT	OR	ON	2006/12/07 12:58
S27	26	714/700.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:00
S28	5	kerveros and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:01
S29	110	"714"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:02
S30	44	"714"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and align\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 13:03
S36	11	"714"/\$.ccls. and PLL and (pulse near3 (supress\$5 disabl\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:05
S34	145	"714"/\$.ccls. and PLL and (pulse clock) with (supress\$5 disabl\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:05
S39	50	(pulse clock) near3 (supress\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:06
S35	94	"714"/\$.ccls. and PLL and (pulse clock) near3 (supress\$5 disabl\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:06
S40	35	(pulse clock) near2 (supress\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:07
S41	34	(pulse) near2 (supress\$5)	US-PGPUB; USPAT	OR	ON	2006/12/07 13:08
S20	66	("714"/\$ "324"/\$.ccls. and reference adj clock and PLL and edge with compar\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 13:16
S42	70	("714"/\$ "324"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and align\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 13:18
S43	28	("714"/\$ "324"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and edge with align\$5	US-PGPUB; USPAT	OR	ON	2006/12/07 13:19
S44	25	("714"/\$ "324"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and edge with align\$5 and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 13:27
S45	22	("714"/\$ "324"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and edge with align\$5 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:06
S46	2	("714"/\$ "324"/\$.ccls. and PLL and (jitter\$3 skew\$3) with (detect\$3 measur\$6) and gated adj (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:08

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S47	324	("714"/\$ "324"/\$).ccls. and gated adj (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:09
S48	5	714/700.ccls. and gated adj (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:10
S50	230	"714"/\$.ccls. and two adj pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:11
S51	4	"714"/\$.ccls. and two adj pulse with (skew or dealy jitter) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:12
S52	3	"714"/\$.ccls. and one adj pulse with (skew or dealy jitter) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:13
S53	143	"714"/\$.ccls. and deskew\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:14
S54	5	"714"/\$.ccls. and clock adj deskew\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:16
S55	8	"714"/\$.ccls. and clock with deskew\$3 with jitter\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:19
S56	4	(jitter\$3 skew\$3) near3 supress\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:21
S57	7	(jitter\$3 skew\$3) with supress\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:23
S59	46	(jitter\$3 skew\$3) with (supress\$3 delet\$3 remov\$3) near2 pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:25
S61	12	("714"/\$ "324"/\$).ccls. and (jitter\$3 skew\$3) with (supress\$3 delet\$3 remov\$3) near2 (pulse clock) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:44
S60	2	("714"/\$ "324"/\$).ccls. and (jitter\$3 skew\$3) with (supress\$3 delet\$3 remov\$3) near2 pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:44
S49	159	"714"/\$.ccls. and gated adj (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:56

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S64	9	"714"/\$.ccls. and (jiter\$ skew\$3) with (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:57
S63	227	"714"/\$.ccls. and (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 17:57
S65	14	"714"/\$.ccls. and (jitter\$ skew\$3) with (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:00
S66	20	("714"/\$ "324"/\$.ccls. and (jitter\$ skew\$3) with (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:03
S69	210	(jitter\$ skew\$3) near3 (remov\$3 supress\$3) near2 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:04
S68	262	(jitter\$ skew\$3) near3 (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:04
S67	296	(jitter\$ skew\$3) with (remov\$3 supress\$3) near3 (clock pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:04
S71	7	(jitter\$ skew\$3) near3 (remov\$3 supress\$3) adj pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:07
S70	34	(jitter\$ skew\$3) near3 (remov\$3 supress\$3) near2 pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:07
S72	113	(jitter\$ skew\$3) and (remov\$3 supress\$3) adj pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:08
S73	13	(jitter\$ skew\$3) same (remov\$3 supress\$3) adj pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:12
S76	252	deskew\$3 near3 (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:13
S75	485	deskew\$3 with (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:13

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S83	60	(jitter\$ skew\$3) with (pulse) near3 remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:15
S82	296	(jitter\$ skew\$3) with (clock pulse) near3 remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:15
S81	399	(jitter\$ skew\$3) same (clock pulse) near3 remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:15
S85	7	(jitter\$ skew\$3) with (pulse) near3 remov\$3 with compensat\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:17
S86	389	(jitter\$ skew\$3) adj remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:18
S87	20	"714"\$.ccls. and (jitter\$ skew\$3) adj remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:19
S91	145	clock adj recover\$4 near2 (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:20
S90	602	clock adj recover\$4 with (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:20
S92	16	clock adj recover\$4 near2 (jitter\$ skew\$3) with pulse NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:21
S97	5	"714"\$.ccls. and clock adj recover\$4 near2 (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:23
S98	75	clock adj recover\$4 near2 (jitter\$ skew\$3) and PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:25
S96	145	clock adj recover\$4 near2 (jitter\$ skew\$3) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:25
S99	6	clock adj recover\$4 near2 (jitter\$ skew\$3) with PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:26
S10 2	1072	(jitter\$ skew\$3) near3 PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:28
S10 0	15	clock adj recover\$4 near2 (jitter\$ skew\$3) same PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:28

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S10 3	35	(jitter\$ skew\$3) near3 PLL with remov\$3 NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:32
S10 8	1385	(jitter\$ skew\$3) near remov\$2 (clock adj pulse) near2 PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:33
S10 7	1385	(jitter\$ skew\$3) near1 remov\$2 (clock adj pulse) near2 PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:33
S10 6	2435	(jitter\$ skew\$3) near3 remov\$2 (clock adj pulse) near2 PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:33
S10 5	2993	(jitter\$ skew\$3) near3 remov\$2 (clock adj pulse) with PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:33
S11 3	21	(jitter\$ skew\$3) with remov\$2 with (clock adj pulse) NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:35
S11 2	2	(jitter\$ skew\$3) with remov\$2 with (clock adj pulse) and PLL NOT Intel ADJ Corporation and @ad<"20040331"	US-PGPUB; USPAT	OR	ON	2006/12/07 18:35
S11 4	232	"714"/\$.ccls. and retransmi\$5 near2 time	USPAT	OR	OFF	2006/12/08 12:40
S11 5	51	"714"/\$.ccls. and retransmi\$5 adj time	USPAT	OR	OFF	2006/12/08 12:41
S11 7	9	"714"/748-750.ccls. and retransmi\$5 adj time and NACK	USPAT	OR	OFF	2006/12/08 12:42
S11 6	37	"714"/748-750.ccls. and retransmi\$5 adj time	USPAT	OR	OFF	2006/12/08 12:42
S1	2	"US 20050229066"	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/03/30 17:31
S11 8	2	(reference with bypass with clock). clm.	US-PGPUB	OR	ON	2007/03/30 17:32